



Contents

RAMSES Series

SPDT up to 18 GHz: R570xxxxxxLP Series	.6-2 to 6-6
DPDT up to 18 GHz: R577xxxxxxxLP Series	-7 to 6-11
SPnT up to 18 GHz: R573xxxxxxLP Series6-	12 to 6-16

LOW PIM PART NUMBER SELECTION GUIDE*

Digita	l Position	R 1-3:	4: RF co	nnectors		5: Type		6: V	oltage	7:	TTL		8: Op	tions		9: Tern	ninals
Series	Configuration		N 12.4 GHz	SMA18GHz	Failsafe	Latching	Normally open*	12 V	28V	Without TTL	With TTL	Without option	Positive common	Supression diodes	Positive common and suppression diodes	Solder pins	D-Sub connector
RAMSES	SPDT	R570LP	1	4	0/1	2/3/5/6	-	2	3	0	1	0	1	3	4	0	5
NAMBLE	DPDT	R577LP	1	4	0/1	2/3/5/6	-	2	3	0	1	0	1	3	4	0	5

Digita	l Position	R 1-3:	4: RF co	nnectors		5: Type		6: Voltage		7: Pos.		8: Options					9: Terr	ninals
Series	Configuration		N 12.4 GHz	SMA 18 GHz	Failsafe	Latching	Normally open*	12 V	28 V	Number of positions	With TTL	Without option	Positive common	TTL Driver	Supression diodes	Positive common and suppression diodes	Solder pins	D-Sub connector
RAMSES	SPnT	R573LP	1	4	-	2/3/4/5/8/9	0/1	2	3	4/6		0	1	2	3	4	0	5

Example of P/N: R573423600LP is a SP6T SMA 18 GHz, latching, 28 Vdc, without option, solder pins.



^{*}For part number creation and available options, see detailed part number selection for each series.



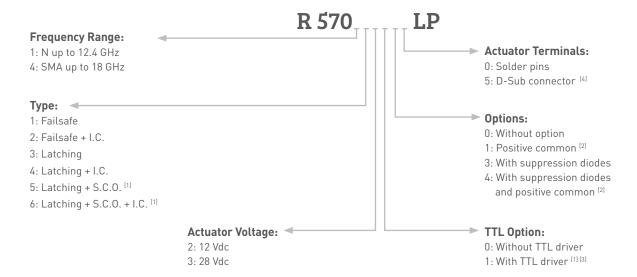
To meet growing market demands created by the deployment of 4G/LTE networks, Radiall has introduced a new range of Low PIM switches. RAMSES SPDT Low PIM switches are perfectly suited for RF test systems and test benches requiring excellent passive intermodulation performance up to 18 GHz; with a guarantee PIM performance of -160 dBc @ +43 dBm over a life span of 2 million switching cycles.

These products are specific to instrumentation and telecommunication applications.

Example of P/N:

R570413030LP is a SPDT Low PIM SMA 18 GHz, failsafe, 28 Vdc, with supression diodes, solder pins.

PART NUMBER SELECTION



I.C.: Indicator contact - S.C.O.: Self Cut-Off

Go online for data sheets & assembly instructions.

- (1): Suppression diodes are already included in Self Cut-OFF & TTL option
- (2): Positive common shall be specified only with type 3, 4, 5 & 6 because failsafe models can be used with both polarities
- (3): Polarity is not relevant to application for switches with TTL driver
- (4): Only available for N models



GENERAL SPECIFICATIONS

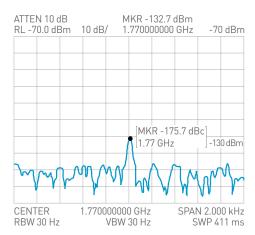
Operating m	ode		Fails	safe	Latching		
Nominal operating voltage (across operating temperature)	, Add		12 (10.2 to 13)	28 (24 to 30)	12 (10.2 to 13)	28 (24 to 30)	
0.11	SMA		47.5	275	58	350	
Coil resistance at 23°C (+/-10%)	N	Ω	38	200	38	225	
Operating current at 23°C	SMA	mA	250	102	210	80	
Operating current at 25 C	N	IIIA	320	140	320	125	
Average power				See Power Rating Cha	art on page 1-16		
High Level			2.2 t	o 5.5 V (TTL Option) / 3.5	to 5.5 V (BCD Option	n)	
TTL input Low Level			0 to 0.8 V (TTL Option) / 0 to 1.5 V (BCD Option)				
Indicator rating				1 Watt / 30 Volts	s / 100 mA		
Switching time		ms		15 ms			
Life (Min)			2 million cycles				
Connectors			SMA - N				
Actuator terminals			Solder pins or male 25 pin D-Sub connector				
Operating temperature range				-40°C to +8	35°C		
Storage temperature range				-55°C to +8	35°C		
Vibration (MIL STD 202, method 204D, cond.D)			10-2000 Hz - 20 g operating				
Shock (MIL STD 202, method 213B, o		100 g / 6 ms - ½ sine operating					

^{*}Reset: supply voltage time 1 sec. max./duty cycle 10%

RF PERFORMANCE

Connectors	Number of positions		uency e GHz	V.S.W.R. (max)	Insertion loss (max) dB	Isolation (min) dB	Impedance Ω	Third order intermodulation
			DC - 3	1.20	0.20	80		
CMA		DC 10	3 - 8	1.30	0.30	70		
SMA		DC - 18	8 - 12.4	1.40	0.40	60		-160 dBc @ +43 dBm (2 carriers 20 W)
	4 and 6		12.4 - 18	1.50	0.50	60	50	
		DC - 12.4	DC - 3	1.20	0.20	80		
N			3 - 8	1.35	0.35	70		
			8 - 12.4	1.50	0.50	60		

OUTSTANDING PIM PERFORMANCE



Passive Intermodulation

Tone 1	1810 MHz, approximately 43 dBm
Tone 2	1850 MHz, approximately 43 dBm
3rd order PIM	160 dBc at 1770 MHz

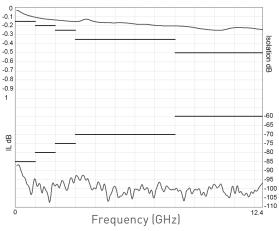
Depending on application, carrier powers and frequencies, PIM measurements can vary. PIM testing is not measured during product acceptance test.



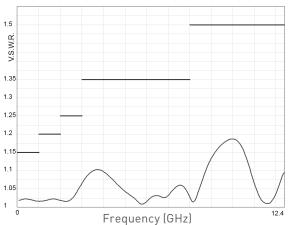
TYPICAL RF PERFORMANCE

Example: SPDT N up to 12.4 GHz

Insertion Loss and Isolation

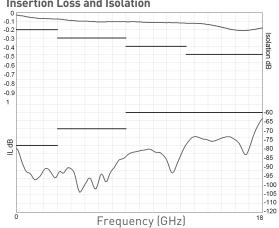


V.S.W.R.

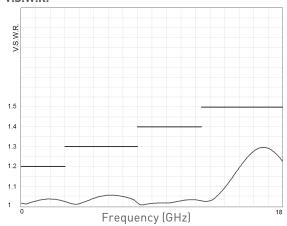


Example: SPDT SMA up to 18 GHz

Insertion Loss and Isolation



V.S.W.R.

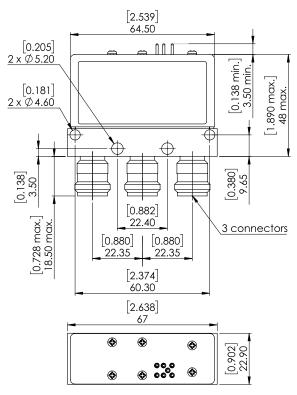


See electrical schematics from page 2-20 to 2-23.

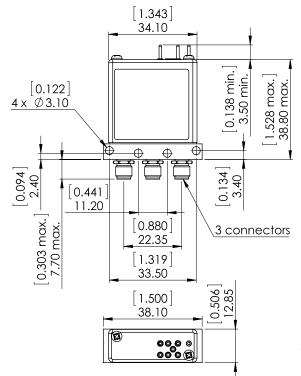


TYPICAL OUTLINE DRAWING

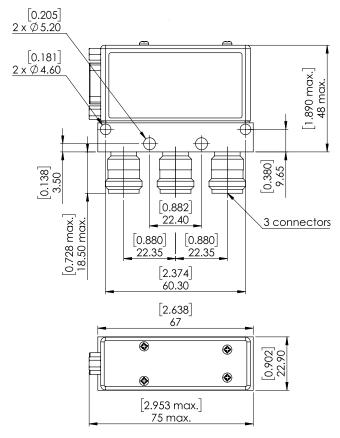
Example: SPDT N up to 12.4 GHz with pins



Example: SPDT SMA up to 18 GHz



Example: SPDT N up to 12.4 GHz with D-sub



All dimensions are in millimeters [inches].





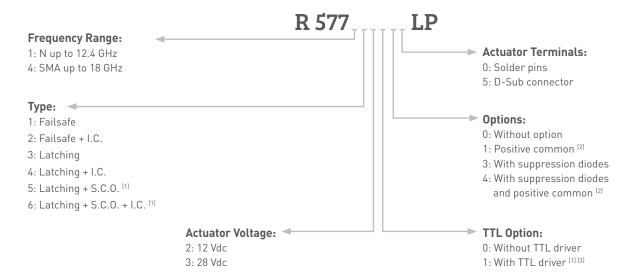
To meet growing market demands created by the deployment of 4G/LTE networks, Radiall has introduced a new range of Low PIM switches. RAMSES DPDT Low PIM switches are perfectly suited for RF test systems and test benches requiring excellent passive intermodulation performance up to 18 GHz; with a guarantee PIM performance of -160 dBc @ +43 dBm over a life span of 2 million switching cycles.

These products are specific to instrumentation and telecommunication applications.

Example of P/N:

R577163105LP is a DPDT Low PIM N 12.4 GHz latching with Indicators, Self Cut-Off, 28 Vdc, TTL driver, D-Sub connector.

PART NUMBER SELECTION



- I.C.: Indicator contact S.C.O.: Self Cut-Off
- (1): Suppression diodes are already included in Self Cut-OFF and TTL option
- (2): Positive common shall be specified only with type 3, 4, 5 and 6 because failsafe models can be used with both polarities
- (3): Polarity is not relevant to application for switches with TTL driver



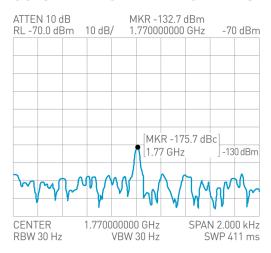
GENERAL SPECIFICATIONS

Operating mode		Norma	lly open	Latching				
Nominal operating voltage (across operating temperature)	Vdc	12 (10.2 / 13)	28 (24 / 30)	12 (10.2 / 13)	28 (24 to 30)			
Coil resistance (+/-10%)	Ω	35	200	38	225			
Nominal operating current at 23°C	mA	340	140	320	125			
Average power			See Power Rating	Chart on page 1-13				
TTI innua		2.2 to 5.5 Volts 8	00 μA max 5.5 Volts					
TTL input	Low Level	0 to 0.8 Volts 20 μA max 0.8 Volts						
Indicator rating		1 W / 30 V	/ / 100 mA					
Switching time (Max)	ms		1	5				
Life (Min)		2 million cycles						
Connectors		SMA - N						
Actuator terminals			Solder pins or male 9	pin D-Sub connector				
Operating temperature range			-40°C t	o +85°C				
Storage temperature range			-55°C t	o +85°C				
Vibration (MIL STD 202, method 204D, cond.	10-2000 Hz, 10 g - operating							
Shock [MIL STD 202, method 213B, cond.G]	50 g / 11 ms - ½ sine operating							

RF PERFORMANCE

Connectors	Frequer	ncy range GHz	V.S.W.R. (max)	Insertion loss (max) dB	Isolation (min) dB	Impedance Ω	Third order intermodulation
		DC - 1	1.15	0.15	85		
		1 - 2	1.20	0.20	80		
Ν	DC - 3 DC - 12.4	2 - 3	1.25	0.25	75		
		3 - 8	1.35	0.35	70		-160 dBc @ +43 dBm (2 carriers 20 W)
		8 - 12.4	1.50	0.50	60	50	
		DC - 3	1.20	0.20	80		
SMA	DC - 3	3 - 8	1.30	0.30	70		
AIVIC	DC - 18	8 - 12.4	1.40	0.40	65		
		12.4 - 18	1.50	0.50	60		

OUTSTANDING PIM PERFORMANCE



Passive Intermodulation

Tone 1	1810 MHz, approximately 43 dBm
Tone 2	1850 MHz, approximately 43 dBm
3rd order PIM	160 dBc at 1770 MHz

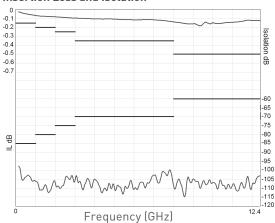
Depending on application, carrier powers and frequencies - PIM measurements can vary. PIM testing is not measured during product acceptance test.



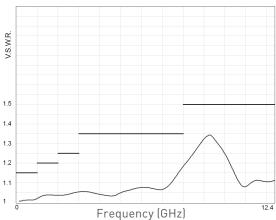
TYPICAL RF PERFORMANCE

Example: DPDT N up to 12.4 GHz

Insertion Loss and Isolation

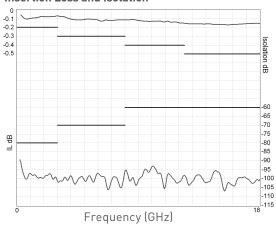


V.S.W.R.

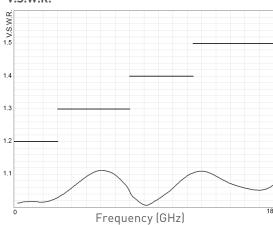


Example: DPDT SMA up to 18 GHz

Insertion Loss and Isolation



V.S.W.R.

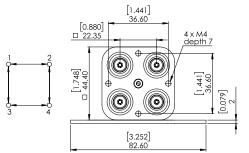


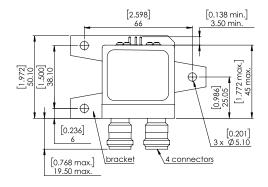
See electrical schematics from page 4-10 to 4-13.



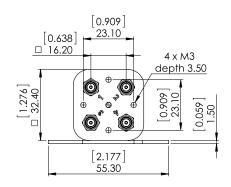
TYPICAL OUTLINE DRAWING

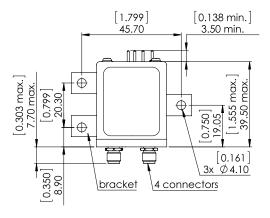
Example: DPDT N up to 12.4 GHz with pins





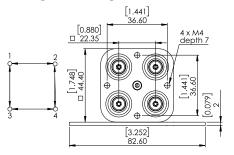
Example: DPDT SMA up to 18 GHz with pins

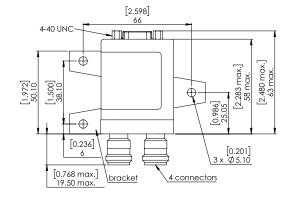




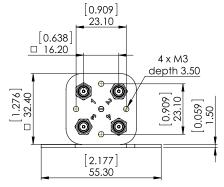
All dimensions are in millimeters [inches].

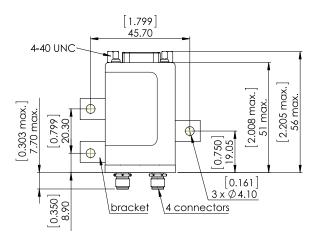
Example: DPDT N up to 12.4 GHz with D-sub





Example: DPDT SMA up to 18 GHz with D-sub









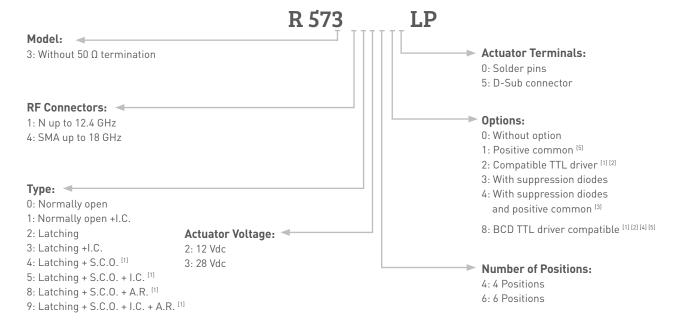
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These products are specific to instrumentation and telecommunication applications.

Example of P/N:

R573403600LP is a SP6T Low PIM SMA up to 18 GHz, Normally Open, 28 Vdc, without option and solder pins.

PART NUMBER SELECTION



- I.C.: Contact / S.C.O.: Self Cut-Off / A.R.: Auto Reset
- (1): These models are already equiped with suppression diodes
- (2): Polarity is not relevant to application for switches with TTL driver
- (3): Option available only for type 0, 1, 2 and 3 $\,$

Go online for data sheets & assembly instructions.

- (4): Latching BCD driver enables also a global reset through driver code 0000 (see BCD logic coding page 1-11)
- (5): Option available only with type 0, 1, 2, 3 and with type 8 and 9 combined with 28 Vdc.



GENERAL SPECIFICATIONS

Type 2, 3, 4 and 5:

Latching models have a RESET pin which commands the reset of all positions. This command should be used before switching from one position to another. If not, two positions will be set at the same time.

Note: During the RESET operation the global current is the nominal operating current multiplied by the number of positions.

Type 8, 9:

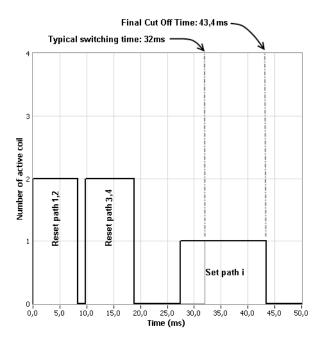
Latching models with AUTOMATIC RESET are available; these products have an internal SET/RESET circuit which automatically resets all the non-selected positions and sets the desired position. This option simplifies the use of latching switches by suppressing the RESET command in switching sequence. An electronic circuit supplies successively groups of 2, 3 or 4 actuators, in order to limit the maximum current. The current with this option is the total current of 2, 3 or 4 reset coils in the same time (see table below).

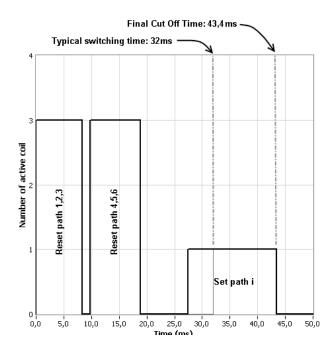
Example: During the AUTOMATIC RESET operation, at 28 Vdc, 4 position switch has a temporary consumption of only 250 mA, during 40 ms maximum.

SWITCHING SEQUENCE

For SP4T

For SP6T





See electrical schematics from page 5-38 to 5-43.



GENERAL SPECIFICATIONS

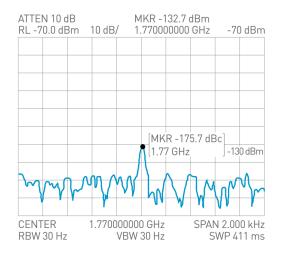
Operating mode		Norm	ally open	Latching					
Nominal operating voltage (across operating temperature)	Vdc	12 (10.2 to 13)	28 (24 to 30)	12 (10.2 to 13)	28 (24 to 30)				
Coil resistance at 23°C (+/-10%)	Ω	47.5	275	38	225				
Nominal operating current at 23°C	mA	250	102	320 Reset SP4T: 1280 mA* Reset SP6T: 1920 mA*	125 Reset SP4T: 500 mA* Reset SP6T: 750 mA*				
Average power			See Power Rating	Chart on page 1-13					
TTI input	High level	2.2 to 5.5 V (TTL Option) / 3.5 to 5.5 V (BCD Option)							
TTL input	Low level		0 to 0.8 V (TTL Option)	/ 0 to 1.5 V (BCD Option)					
Indicator rating			1 W / 30 Y	V / 100 mA					
Switching time (Max)	ms			5 eset models: 40					
Life (Min)		2 million cycles							
Connectors			SMA	4 - N					
Actuator terminals			Solder pins or male 2	5 pin D-Sub connector					
Operating temperature range			-25°C t	o +70°C					
Storage temperature range		-55°C to +85°C							
Vibration (MIL STD 202, method 2040), cond.D)	10-2000 Hz , 20 g operating							
Shock (MIL STD 202, method 213B, c	ond.C)	100 g / 6	100 g / 6 ms, ½ sine operating						

^{*}Reset: supply voltage time 1 sec. max./duty cycle 10%

RF PERFORMANCE

Connectors	Number of positions	Frequency	range GHz	V.S.W.R. (max)	Insertion loss (max) dB	Isolation (min) dB	Impedance Ω	Third order intermodulation											
			DC - 3	1.20	0.20	80													
SMA		DC - 18	3 - 8	1.30	0.30	70													
SIVIA		DC - 18	DC - 10	DC - 10	DC - 10	DC - 10	DC - 10	DC - 10	8 - 12.4	1.40	0.40	60		-160 dBc @ +43 dBm					
	4 and 6		12.4 - 18	1.50	0.50	60	50	(2 carriers 20 W)											
			DC - 3	1.20	0.20	80													
N			DC - 12.4	DC - 12.4	DC - 12.4	DC - 12.4	DC - 12.4	DC - 12.4	DC - 12.4	DC - 12.4	DC - 12.4	DC - 12.4	DC - 12.4	DC - 12.4	3 - 8	1.35	0.35	70	
			8 - 12.4	1.50	0.50	60													

OUTSTANDING PIM PERFORMANCE



Passive Intermodulation

Tone 1	1810 MHz, approximately 43 dBm
Tone 2	1850 MHz, approximately 43 dBm
3rd order PIM	160 dBc at 1770 MHz

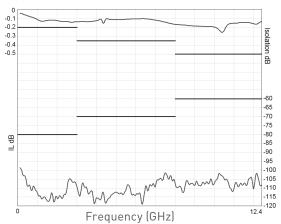
Depending on application, carrier powers and frequencies — PIM measurements can vary. PIM testing is not measured during product acceptance test.

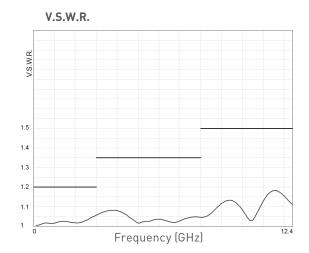


TYPICAL RF PERFORMANCE

Example: SP6T N up to 12.4 GHz

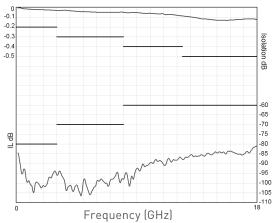
Insertion Loss and Isolation

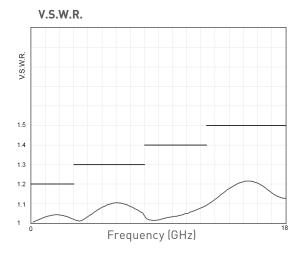




Example: SP6T SMA up to 18 GHz

Insertion Loss and Isolation



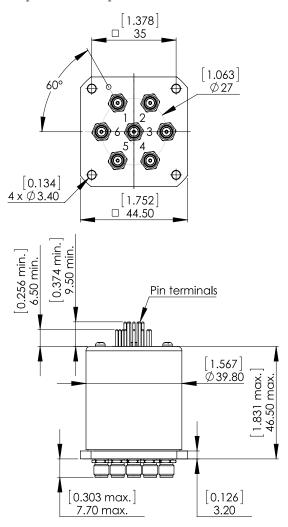


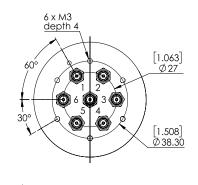


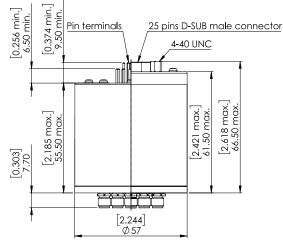
6-13

TYPICAL OUTLINE DRAWING

Example: SPnT SMA up to 18 GHz







Solder	Type 0 or 1 with option 0 - 1 - 3 or 4		
pins	Type 2 or 3 with option 0 or 1		

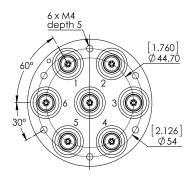
All dimensions are in millimeters [inches].

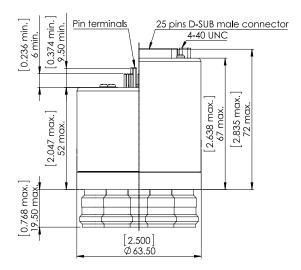
Solder pins	Type 0 or 1 with option 2 or 8		
	Type 2 or 3 with option 2 - 3 - 4 or 8		
	Type 4 - 5 - 8 or 9 with option 0 - 2 or 8		

D-Sub connector	All models
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Example: SPnT SMA up to 12.4 GHz

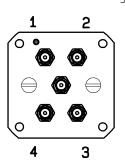


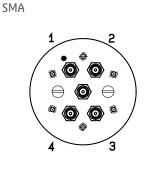


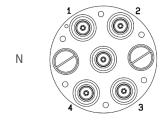
All dimensions are in millimeters [inches].

RF CONNECTORS ALLOCATION

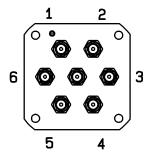
SP4T



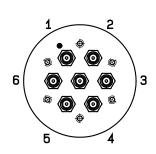


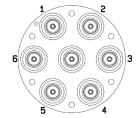






Ν





SMA



Coaxial Low PIM Switches - Electrical Schematics

Type Failsafe		Failsafe	Latching			
Options		Without option	Without option	Cut-off	C+ and suppression diodes	
		Indicator contact	Indicator contact	Cut-off and I.C.	C+, suppression diodes and I.C.	
		Suppression diodes	Suppression diodes	Cut-off and TTL Driver	C+ and cut-off	
		Suppression diodes and I.C.	Suppression diodes and I.C.	Cut-off, TTL and I.C.	C+, cut-off and I.C.	
		TTL Driver	TTL Driver	C+	-	
		TTL Driver and I.C.	TTL Driver and I.C.	C+ and I.C.		
Page Number	SPDT	see page 2-20	see page 2-21	see page 2-22	see page 2-23	
	DPDT	see page 4-10	see page 4-11	see page 4-12	see page 4-13	

Type Normall		ly open		Lato	Latching		
		Without option	BCD TTL driver	Without option	Cut-off	TTL Driver, Cut-off and Auto reset	C+ and suppres- sion diodes
Options		Indicator contact	BCD TTL driver and I.C.	Indicator contact	Cut-off and I.C.	TTL Driver, Cut- off, Auto reset and I.C.	C+, suppression diodes and I.C.
		Suppression diodes	C+	Suppression diodes	Cut-off and Auto reset	BCD TTL Driver, Cut-off and Auto reset	C+, Cut-off and Auto reset
	ns	Suppression diodes and I.C.	C+ and I.C.	Supression diodes and I.C.	Cut-off, Auto reset and I.C.	BCD TTL Driver, Cut-off, Auto reset and I.C.	C+, Cut-off, Auto reset and I.C.
		TTL Driver	C+ and suppression diodes	TTL Driver	Cut-off and TTL Driver	C+	-
		TTL Driver and I.C.	C+, suppression diodes and I.C.	TTL Driver and I.C.	Cut-off, TTL and I.C.	C+ and I.C.	-
Page Number	SPnT	see page 5-38	see page 5-39	see page 5-40	see page 5-41	see page 5-42	see page 5-43

